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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/986,727	11/09/2001	Chien- Ming Chen	0941-0364P-SP	5871	
2292	7590 02/22/2005		EXAMINER		
BIRCH STE	WART KOLASCH &	DU, THUAN N			
	RCH, VA 22040-0747	ART UNIT	PAPER NUMBER		
			2116		
			DATE MAILED: 02/22/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

		Applica	ation No.	Applicant(s)			
			5,727	CHEN ET AL.			
Office Action Summary		Examir	ner	Art Unit			
		Thuan	N. Du	2116			
	The MAILING DATE of this commu	nication appears on	the cover sheet w	ith the correspondence ac	idress		
Period f	• •			•			
THE - External control	MAILING DATE OF THIS COMMUNICATION OF THIS C	NICATION. ns of 37 CFR 1.136(a). In no nmunication. (30) days, a reply within the statutory period will apply and ly will, by statute, cause the	e event, however, may a statutory minimum of thi d will expire SIX (6) MOI application to become A	reply be timely filed rty (30) days will be considered timel NTHS from the mailing date of this c BANDONED (35 U.S.C. § 133).			
Status							
1) 🛛	Responsive to communication(s) fi	led on 02 December	r 2004.				
2a)□							
3)□							
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposit	ion of Claims						
_	Claim(s) <u>1-7</u> is/are pending in the a 4a) Of the above claim(s) is/ Claim(s) <u>4-7</u> is/are allowed.		consideration.				
·	Claim(s) <u>4-7</u> is/are allowed. Claim(s) <u>1-3</u> is/are rejected.						
7)	Claim(s) is/are objected to.						
· —	Claim(s) are subject to restr	iction and/or election	n requirement.				
Applicat	ion Papers						
	The specification is objected to by t	he Examiner		•			
'=	The drawing(s) filed on is/are		b) objected to	by the Examiner.			
,—	Applicant may not request that any obj		· ·	•			
	Replacement drawing sheet(s) including	ng the correction is req	uired if the drawing	ı(s) is objected to. See 37 C	FR 1.121(d).		
11)	The oath or declaration is objected	to by the Examiner.	Note the attache	d Office Action or form P	ΓΟ-152.		
Priority :	under 35 U.S.C. § 119						
•	Acknowledgment is made of a claim All b) Some * c) None of: Certified copies of the priority Certified copies of the priority Copies of the certified copies application from the Internation	y documents have b y documents have b s of the priority docu	een received. een received in A ments have beer	Application No	Stage		
* (See the attached detailed Office acti	•	`	received.			
Attachmer	nt(s)						
1) 🔯 Notic	ce of References Cited (PTO-892)			Summary (PTO-413)			
	ce of Draftsperson's Patent Drawing Review (mation Disclosure Statement(s) (PTO-1449 o			s)/Mail Date Informal Patent Application (PT0	O-152)		
	er No(s)/Mail Date	, , , , , , , , , , , , , , , , , , ,	6) Other:		- · · · · ·		

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DETAILED ACTION

1. Claims 1-7 are presented for examination.

2. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.

Claim Rejections - 35 USC § 102

- 3. Claims 1-3 are rejected under 35 U.S.C. 102(b) as being anticipated by Strahlin (U.S. Patent No. 5,758,132).
- 4. Regarding claim 1, Strahlin teaches a method for reducing clock skew in an integrated circuit having a plurality of circuit blocks (16, 18), the method comprising:

providing a first source clock (clock circuitry 26) coupled to a clock input terminal first circuit block (16) within the circuit blocks [Fig. 1; col. 3, lines 61-64];

providing a second source clock (clock circuitry 28) coupled to a clock input terminal of second circuit block (18) within the circuit blocks [Fig. 1; col. 3, lines 61-64]; and

switching the clock input terminal of the second circuit block the first source clock when the second circuit block is configured to operate in synchronization with the first circuit block [col. 2, lines 30-44; col. 7, lines 42-67], thereby operating the first and second circuit blocks in accordance with the same first source clock [col. 4, lines 20-22].

5. Regarding claim 2, Strahlin further teaches:

providing an operation mode signal [col. 7, lines 15-18]; and

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setting a first state to the operation mode signal (PWOS = 1), thereby operating second circuit block synchronization with the first circuit block [col. 7, lines 15-18; col. 8, lines 48-49].

6. Regarding claim 3, Strahlin further teaches:

setting a second state to the operation mode signal, wherein the second circuit block operates asynchronously to the first circuit block when the operation mode signal is at the second state [col. 4, lines 40-41; col. 7, lines 15-18; col. 8, lines 37-39]; and

switching the clock input terminal the second circuit block to second source clock, thereby independently operating the first circuit block in accordance with the first source clock and the second circuit block in accordance with the second source clock [col. 4, lines 42-45].

Allowable Subject Matter

7. Claims 4-7 are allowed.

Conclusion

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thuan N. Du whose telephone number is (571) 272-3673. The examiner can normally be reached on Monday and Wednesday-Friday: 9:30 AM - 8:00 PM, EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne H. Browne can be reached on (571) 272-3670.

Central TC telephone number is (571) 272-2100.

The fax number for the organization is (703) 872-9306.

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9. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll free).

Thuan N. Du

February 14, 2005